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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Christopher H. Pham
Assignee:	Cisco Technology, Inc.
Title:	Linear Associative Memory-Based Hardware Architecture For Fault Tolerant ASIC/FPGA Workaround

Serial No.:	09/837,882	Filing Date:	July 22, 1999
Examiner:	Mai T. Tran	Group Art Unit:	2129
Docket No.:	CIS0115US	Client Ref. No.:	3001

Austin, Texas
November 2, 2005

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the final Office action dated October 19, 2005, having a shortened statutory period set to expire January 19, 2206. Further examination and consideration are requested.

No Amendments to the Specification are presented in this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

No Amendments to the Drawings are presented in this paper.

Remarks begin on page 7 of this paper.

OK

11/22/05